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APPLICANTS

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** CONTINUING DATA *****

None, TL

** FOREIGN APPLICATIONS *****

JAPAN 2002-353731 12/05/2002

yes, TL

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| 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | | | | |
| Verified and Acknowledged Examiner's Signature _____ Initials _____ | | | | |

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TITLE

Semiconductor memory device and method for correcting memory cell data

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